01 FC:1252

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

	First-Named Inve	entor: LEDIEN, Jeroen	Docket No.:	NL 010073	
	Application No.:	10/059427 Conf.:	Art Unit:	2183	
	Date Filed:	01/29/2002	Examiner:	Rizzuto, Kevin	
	Title: COMPUTER INSTRUCTION WITH INSTRUCTION FETCH CONTROL BITS				
	Commissioner fo	or Patents		.*	
	P.O. Box 1450	2212 1450			
•	Arlington, VA 22		TAL LETTER	•	
·	Sir:	ANALYSIMAA	ARULEITER	•	
	There are	a total of 2 pages in this submis	sion which includ	les this transmittal letter, as	well
	as the documents	listed below:		,	<u>*</u>
	⊠ R	esponse to Restriction Requirem	ent - 1 page	•	
	片 -			<u> </u>	•
•	The Commissioner is hereby requested and authorized pursuant to 37 CFR §1.136(a)(3), to treat any concurrent or future reply in this application requiring a petition for extension of time for its				
	•				
	timely submissio	n, as incorporating a petition for	extension of time	e for the appropriate length of	f
	time. Please cha	rge any additional fees which ma	y now or in the f	uture be required in this	
		iding extension of time fees, but			ested
	••	lit any overpayment, to Deposit	_	_	
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	Date:(/2	<i>loy</i> 1	Respectfully subr	nitted,	
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		1	By Alel		
		•		e, Reg. No. 33,089	_
			(408) 474-907	of The second second	
				onics North America Corp.	
•				Drive, M/S 41-SJ	
			San Jose, CA	95131	
		CERTIFICATE OF MA	LING or TRANSMI	SSION BOOK	
		I hereby certify that this correspondence Service with sufficient postage for first class or	anii ka een anseelaandalleleess	ed to "Committioner I	
	,	for Patents, P.O. Box 1450, Arlington, VA 22 USPTO at (703) 872-9306, on the date indicate	313-1450," or fodsithile u	anumitted to the	
		Maland	(V IX_		
\$4 IAO III AA F WALL		(Name)	Daniel LAtuchalek		
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